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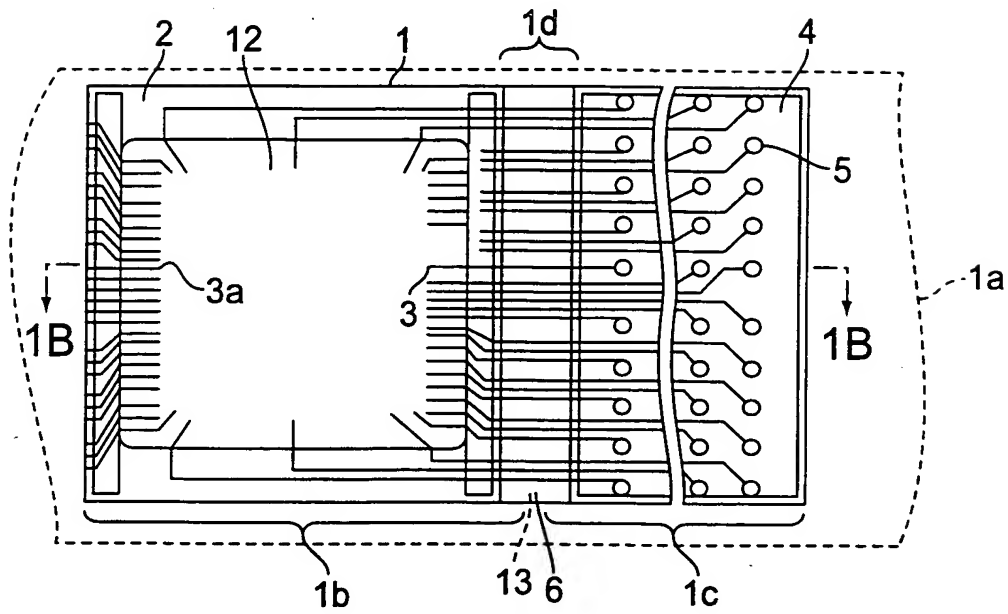


FIG. 1A

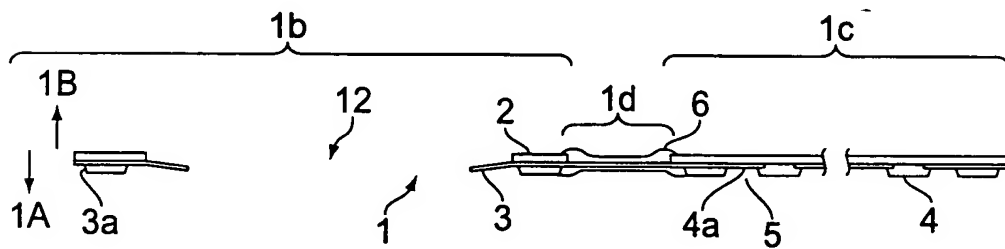


FIG. 1B

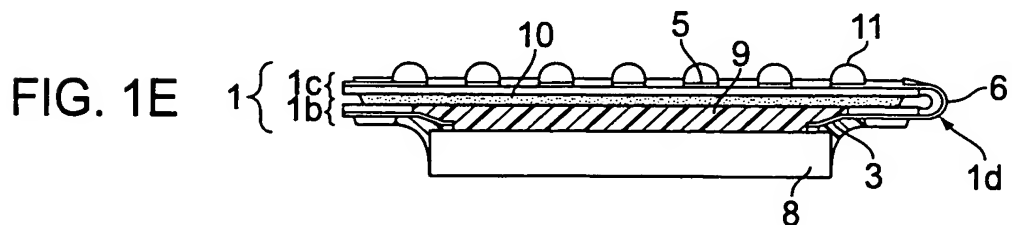
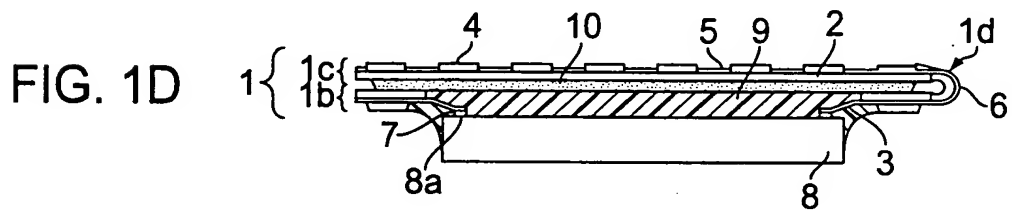
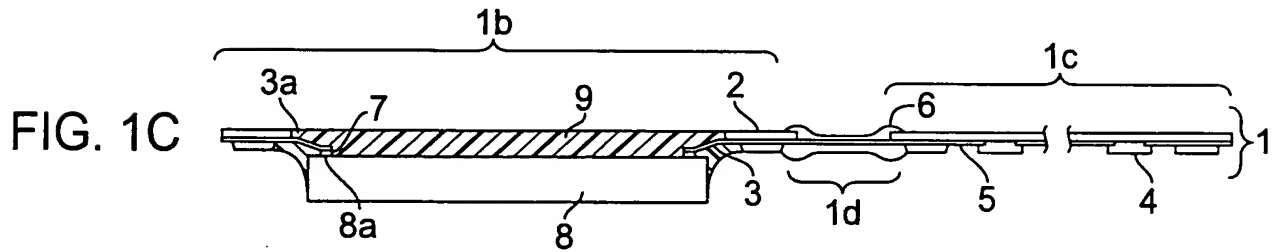


FIG.2A

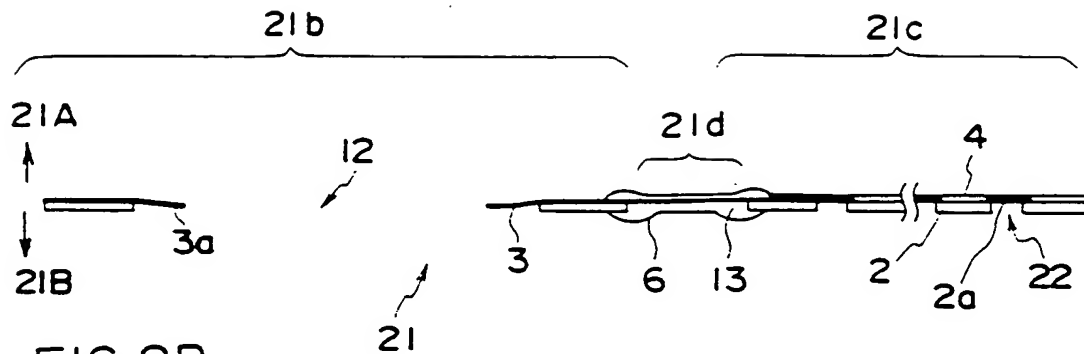
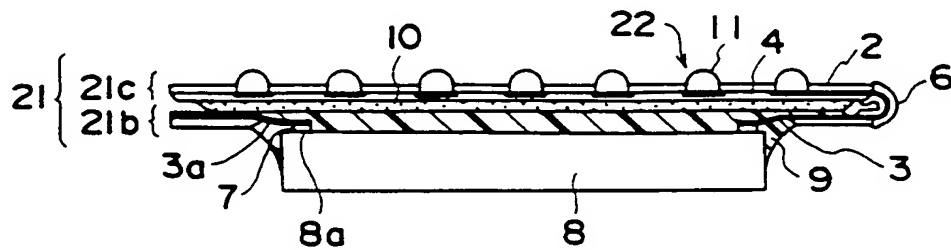


FIG.2B



Inventor: Yoshikazu TAKAHASHI
 Title of Invention: Semiconductor Device
 Having a Chip-Size Package
 Attorney Docket No.: 32305-200192
 VENABLE

FIG. 3A

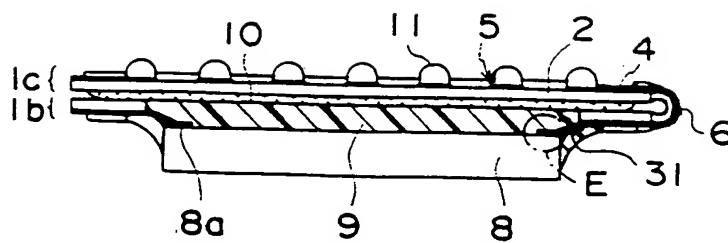
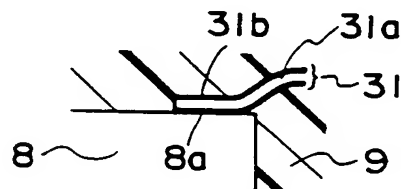


FIG. 3B



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FIG.4A

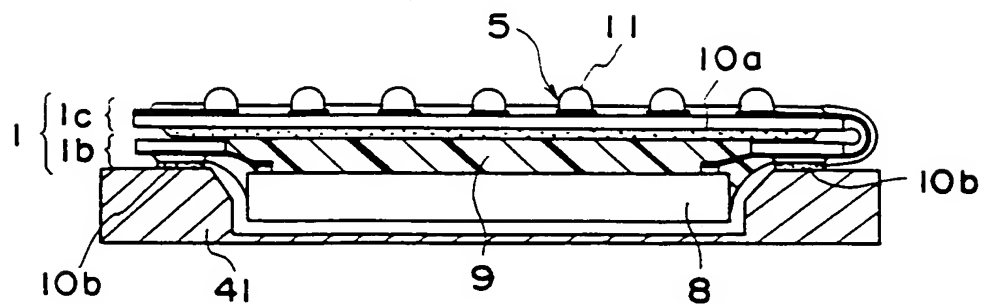
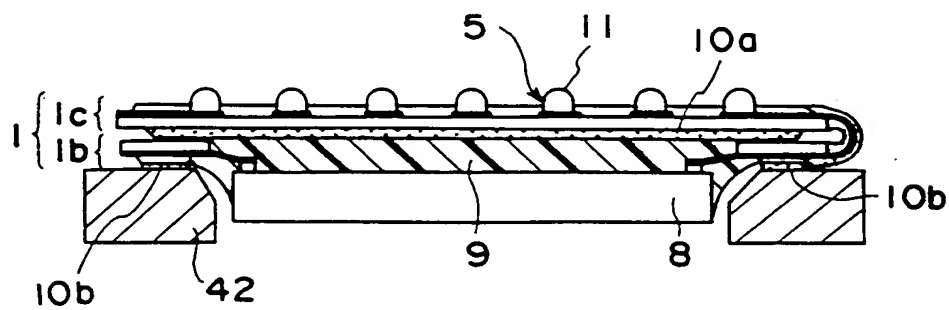


FIG.4B



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FIG. 5

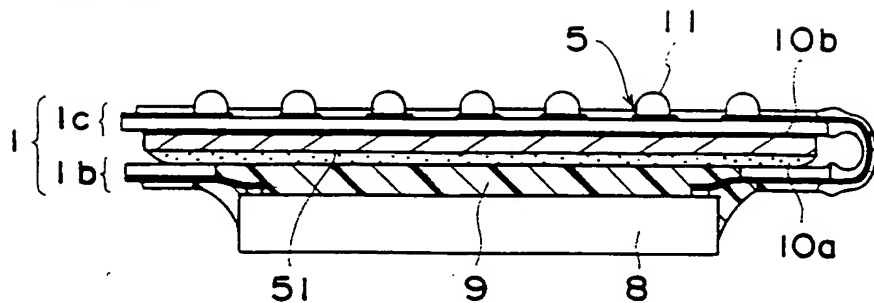
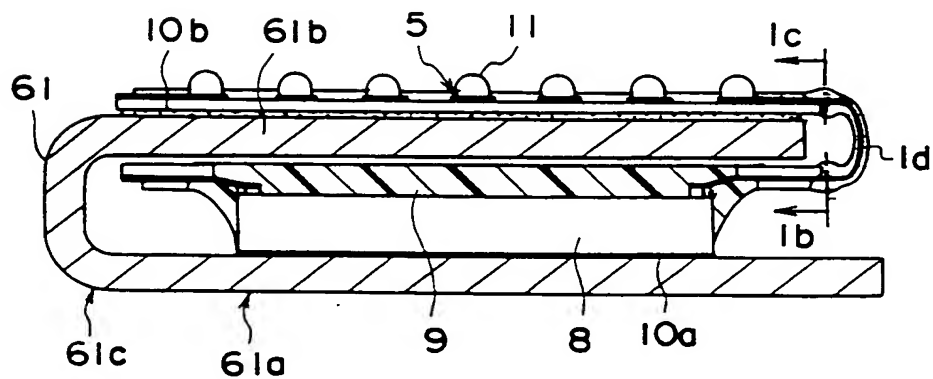


FIG. 6



9

FIG. 7A

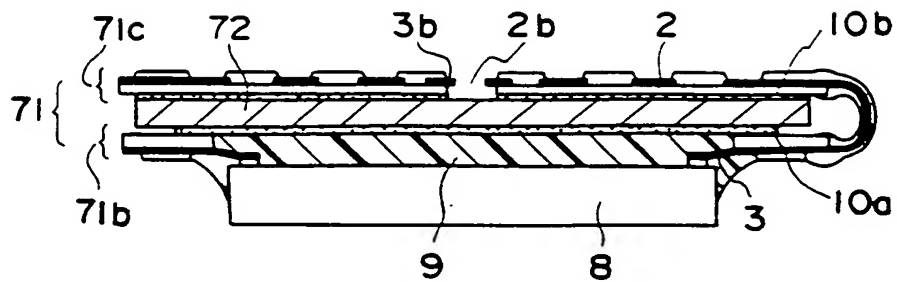
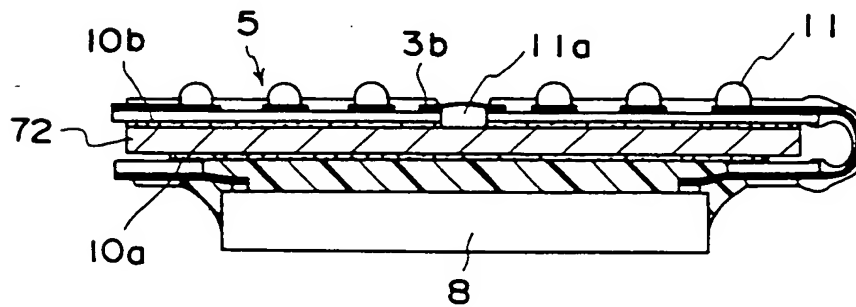


FIG. 7B



VENABLE

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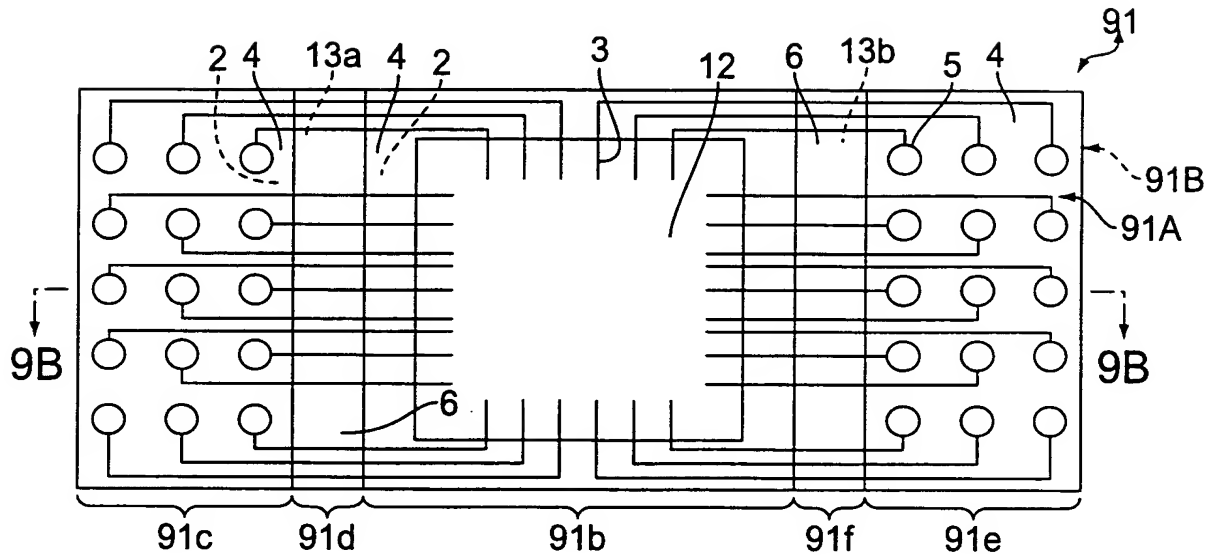


FIG. 9A

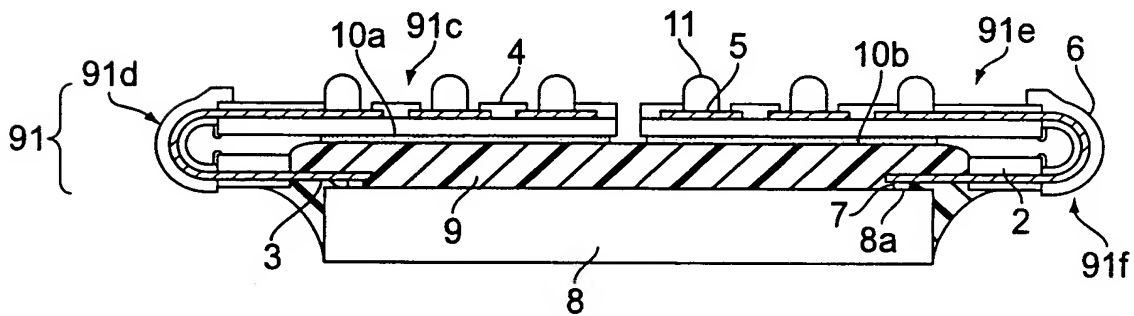


FIG. 9B

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FIG. 10

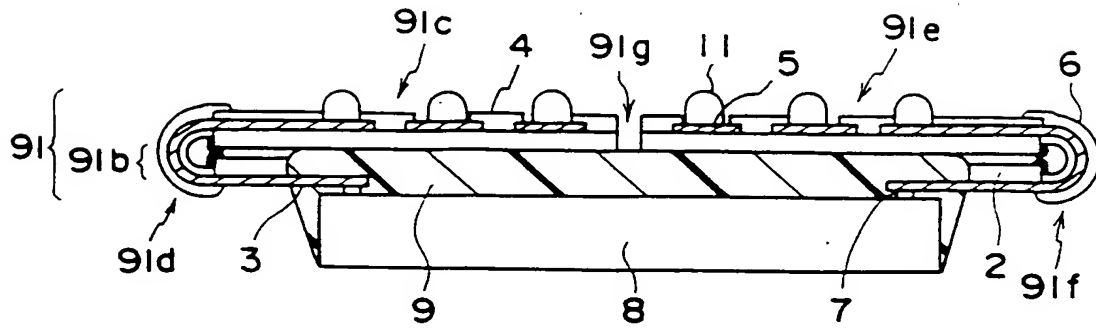


FIG. 11

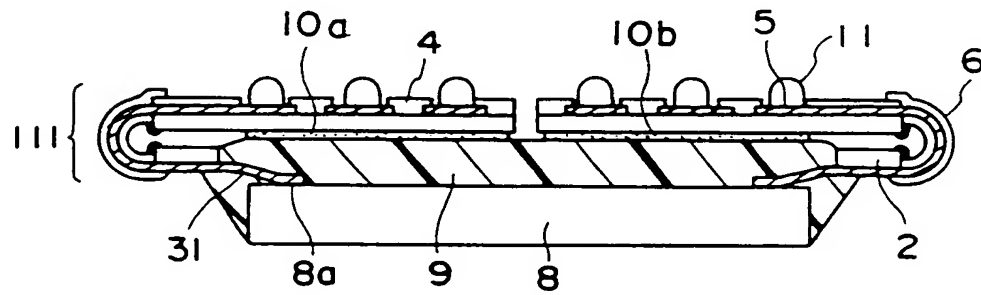


FIG.12A

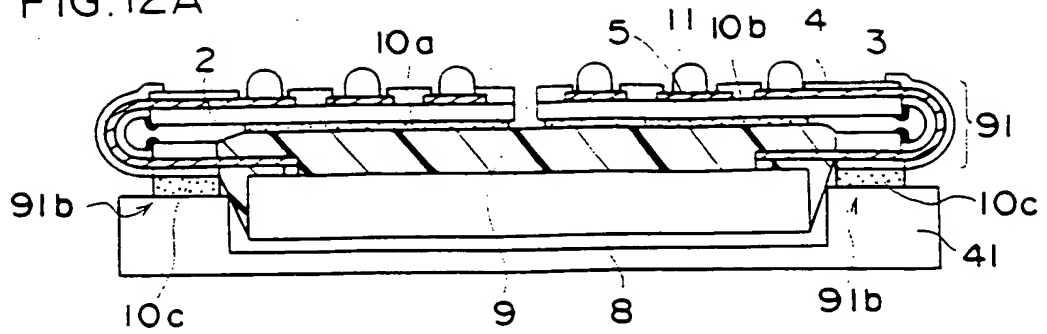
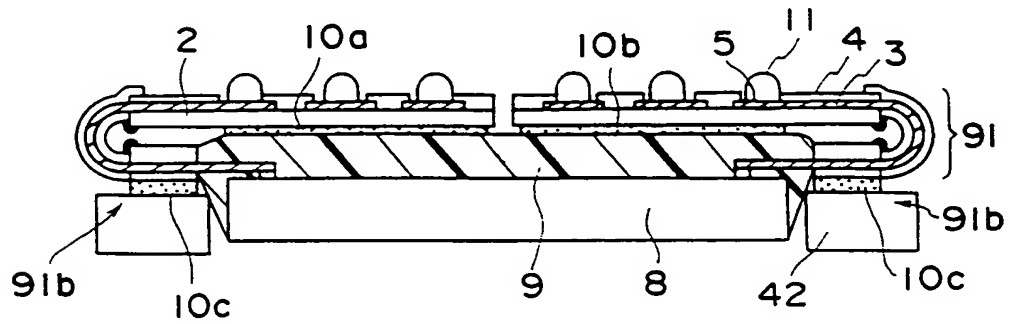


FIG.12B



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FIG. 13

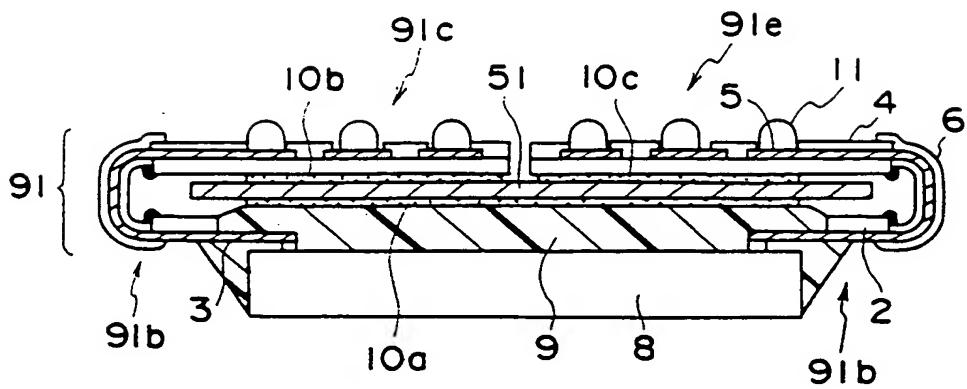
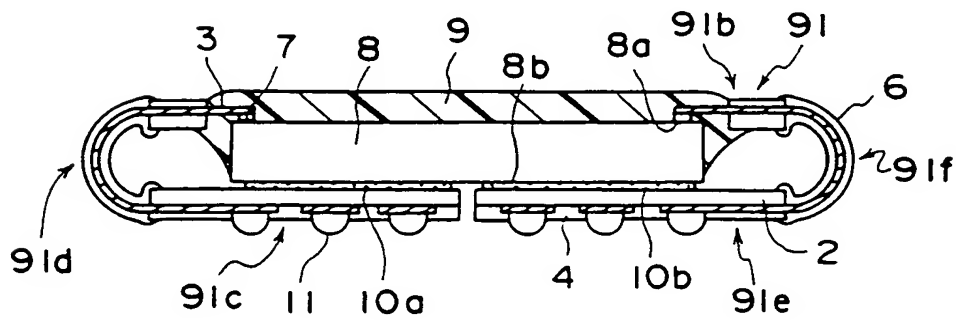


FIG. 14



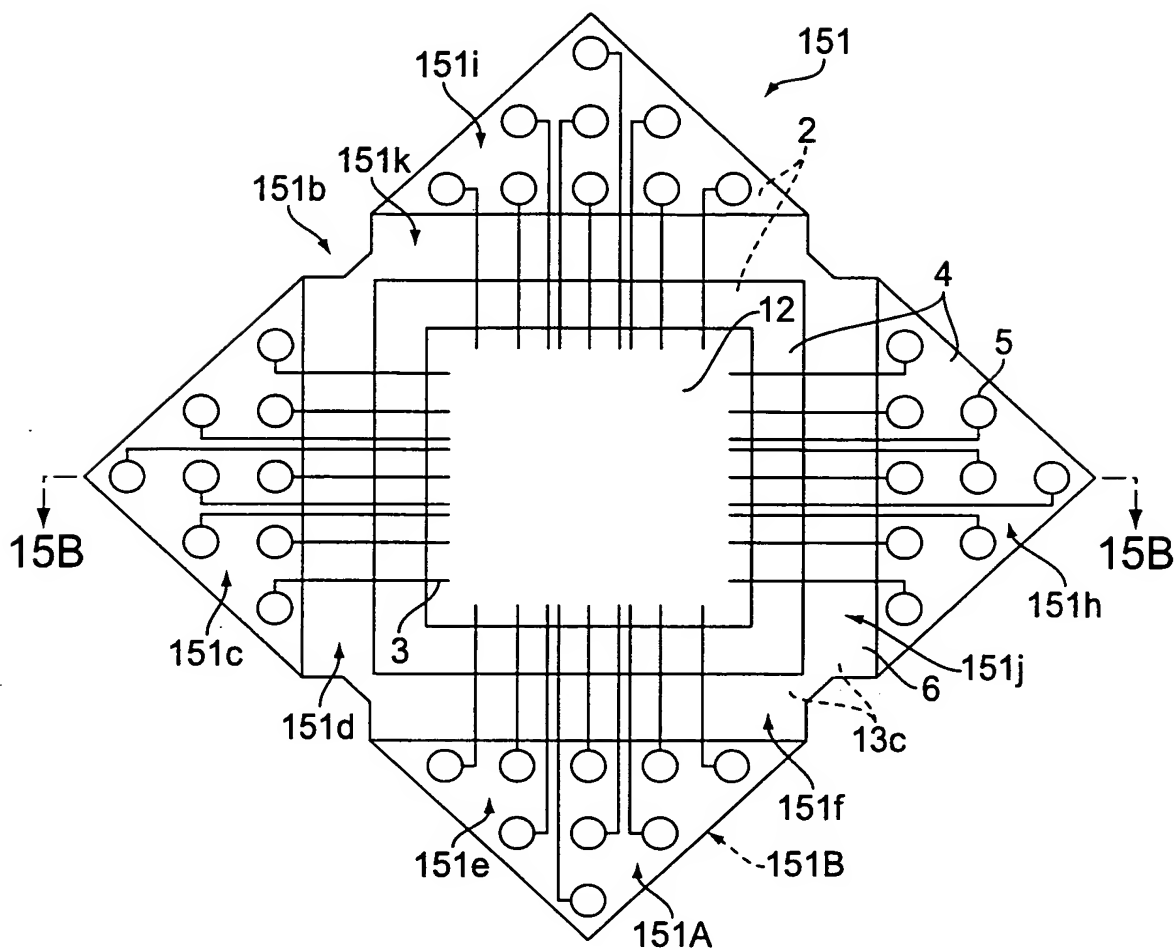


FIG. 15A

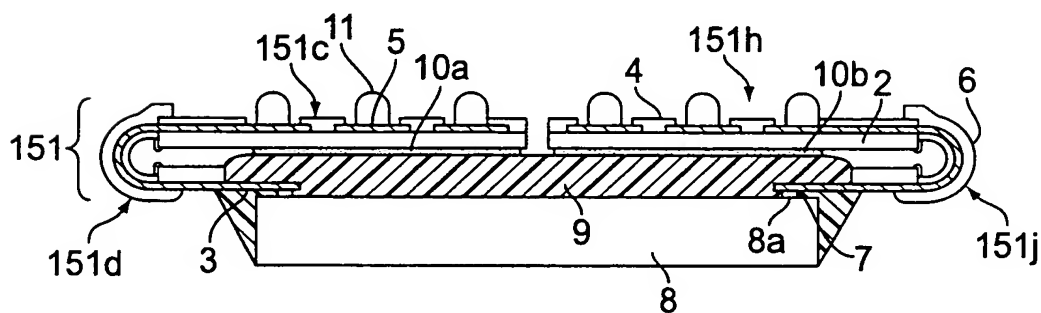


FIG. 15B

FIG.16A

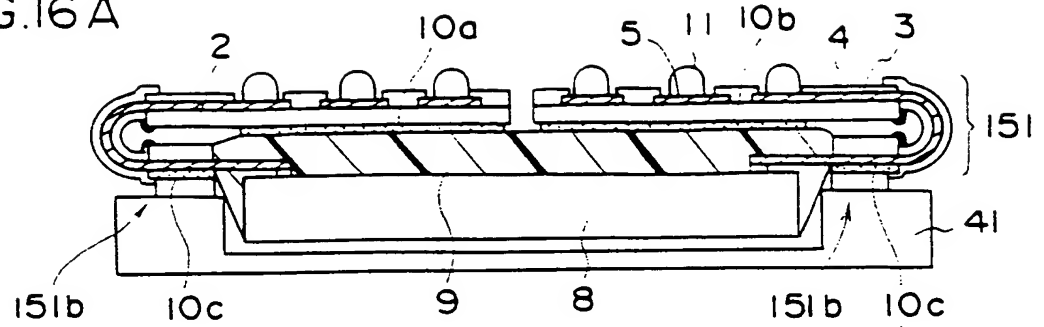
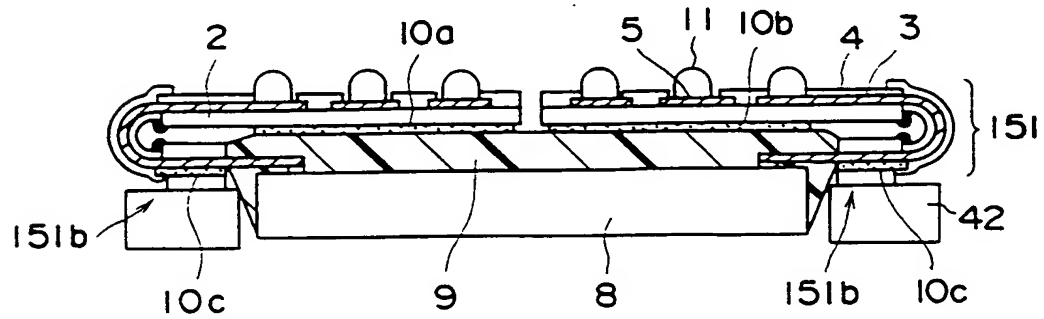
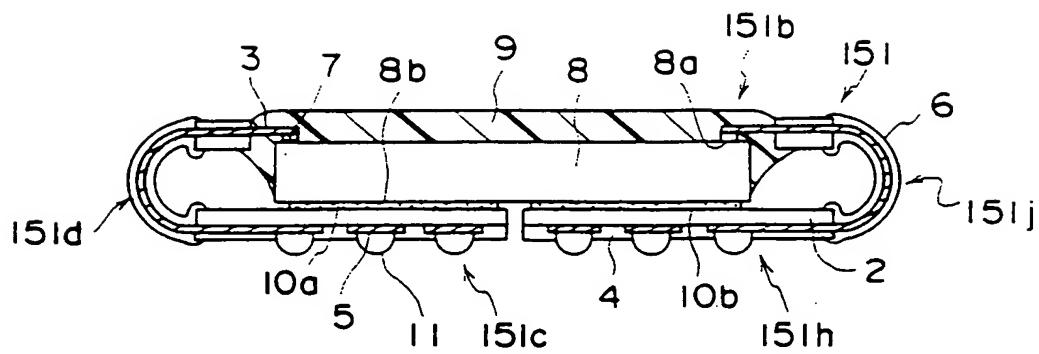


FIG.16B



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FIG. 17



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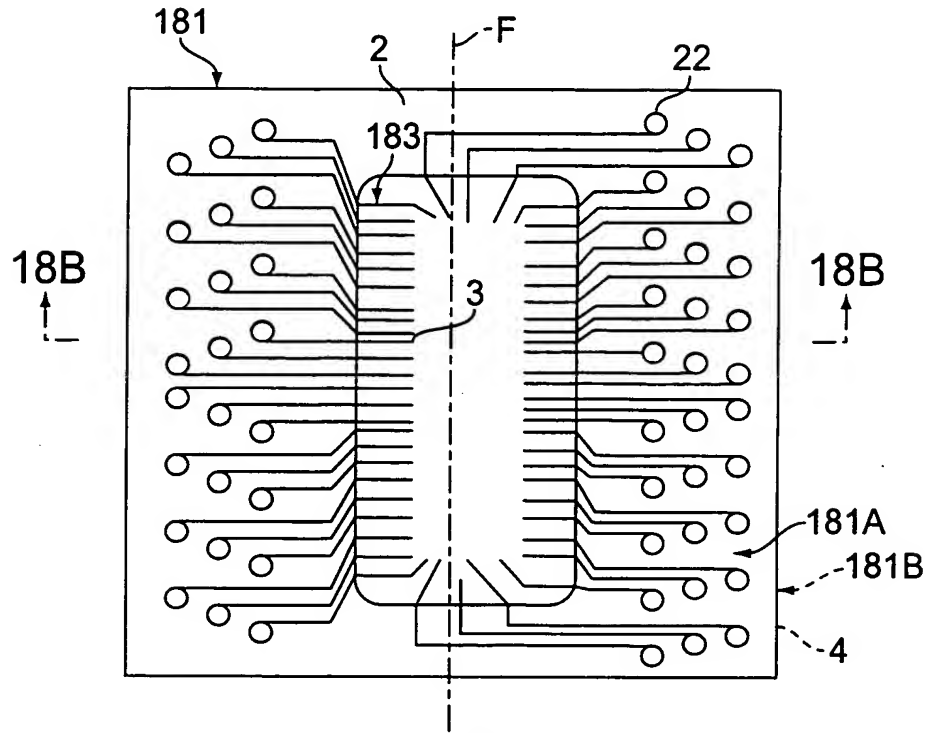


FIG. 18A

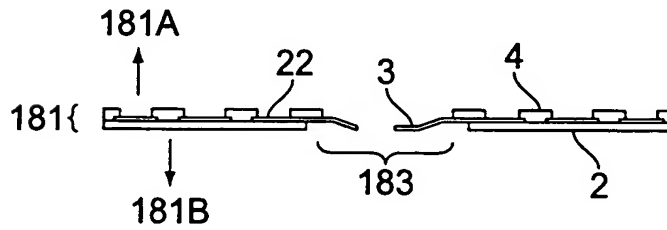


FIG. 18B

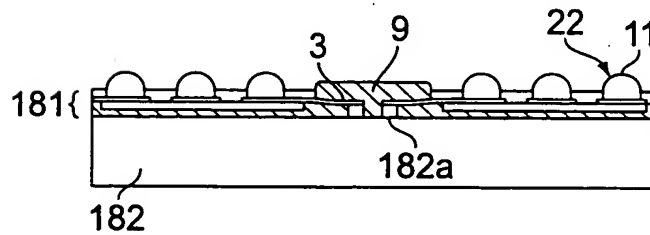


FIG. 18C

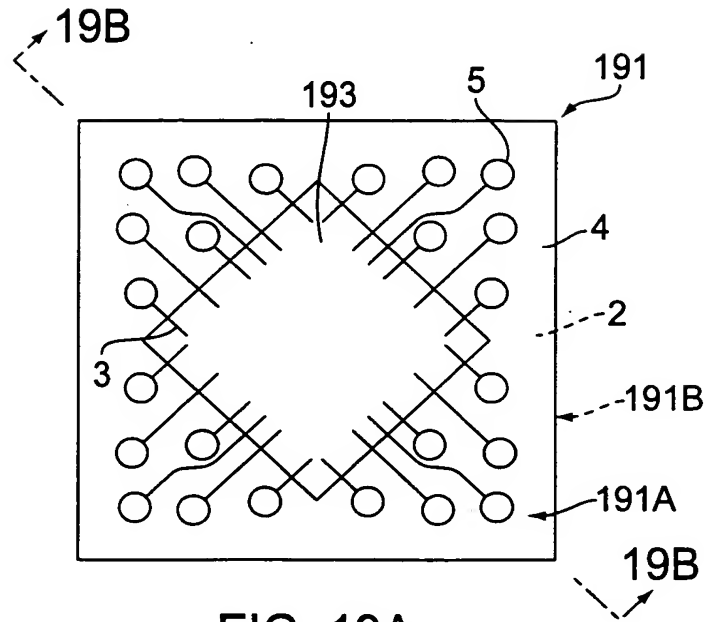


FIG. 19A

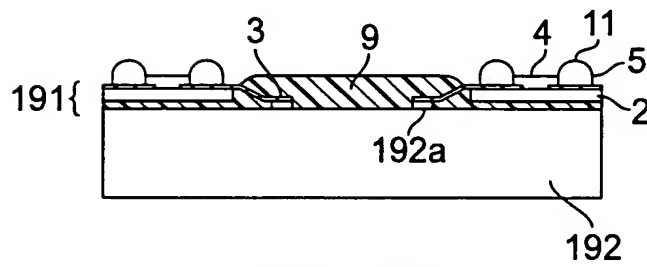


FIG. 19B

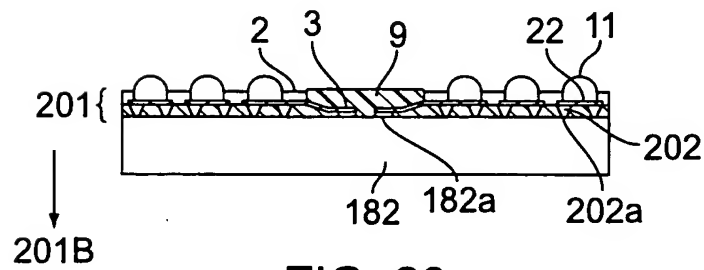


FIG. 20

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FIG.21A

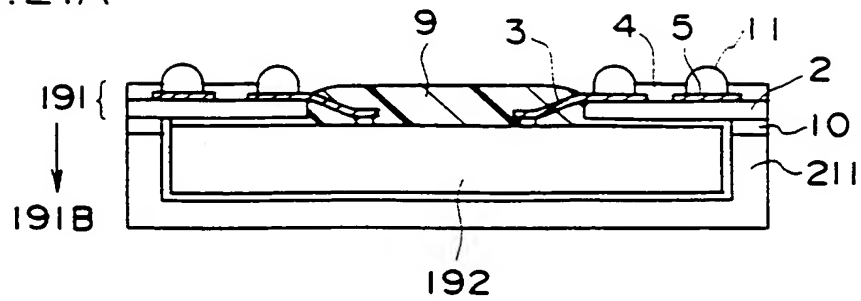


FIG.21B

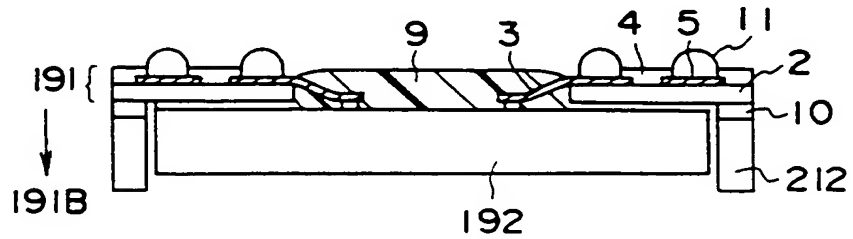
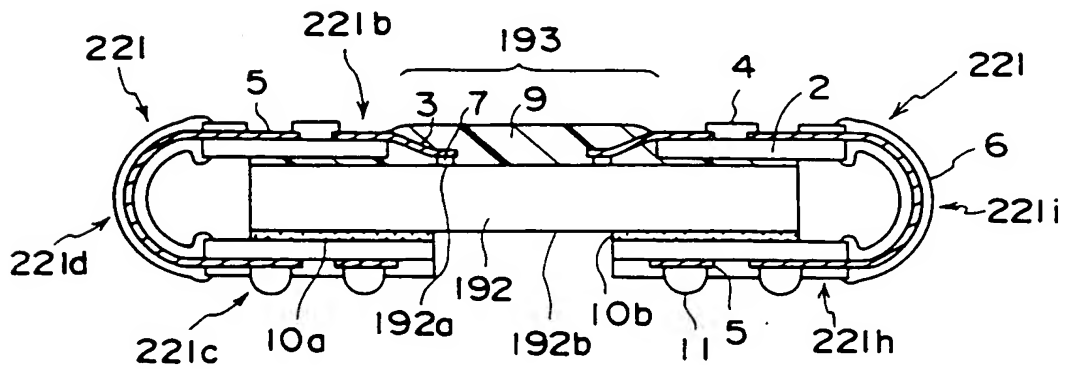


FIG.22



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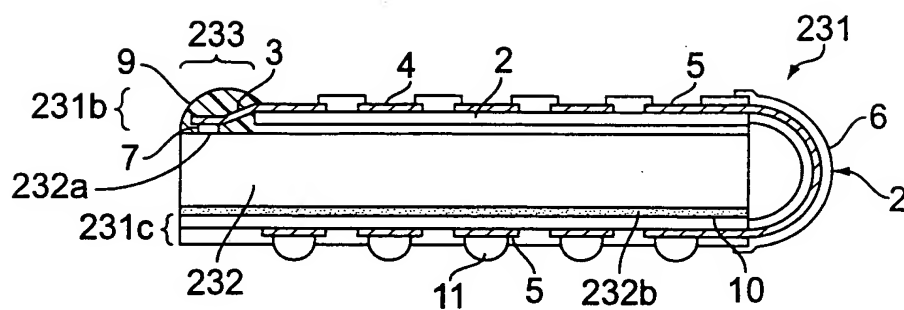


FIG. 23

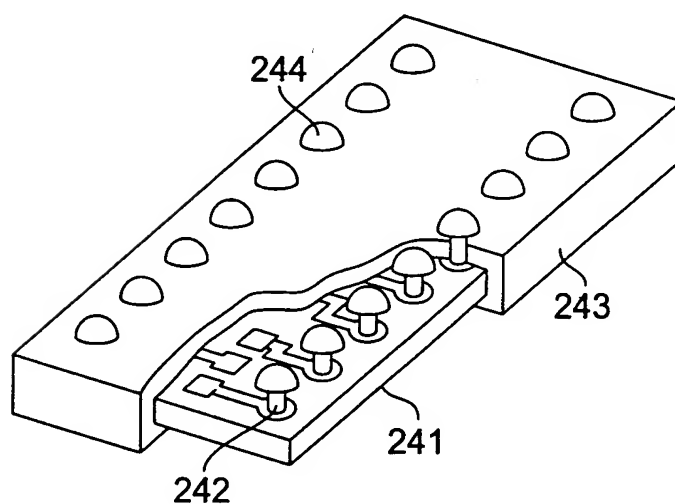


FIG. 24
(PRIOR ART)

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FIG. 25
PRIOR ART

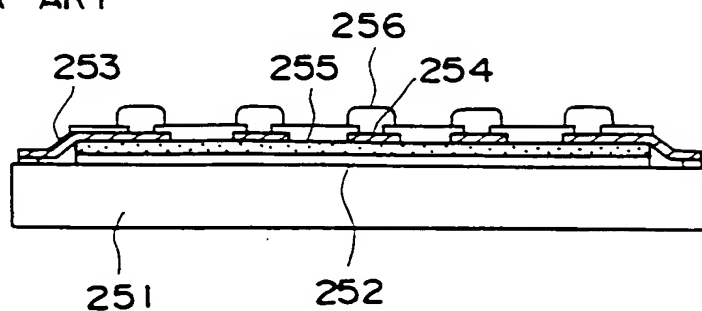


FIG. 26
PRIOR ART

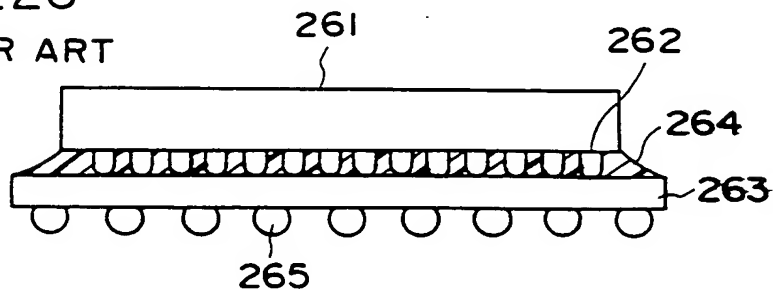


FIG.27A

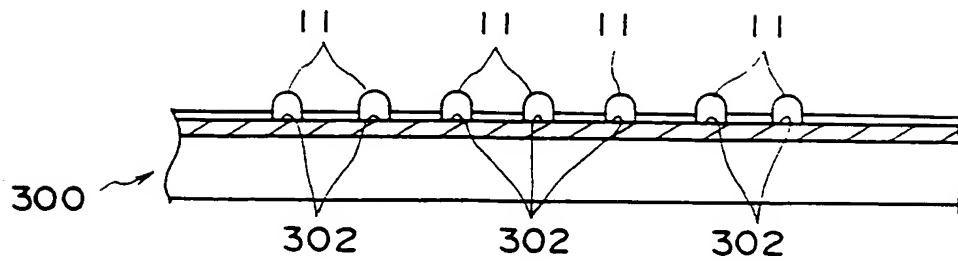


FIG.27B

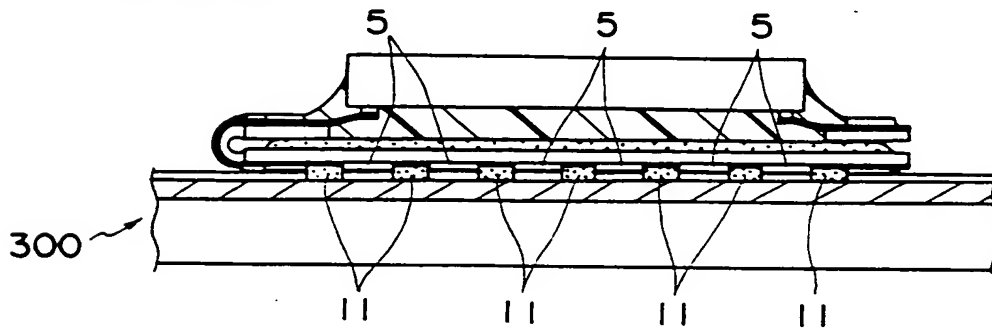


FIG.28

